

ABSTRACT OF THE DISCLOSURE

An image data conversion method and apparatus capable of real time output of moving pictures and simplifying the control circuit. This apparatus includes a central processing unit (CPU) for decoding compressed data and transferring the decoded data at the same line frequency as that of a receiver, and an image inverse-conversion processor for converting the data transferred from the CPU to an image size that can be displayed on the receiver. The CPU controls the decoded data so that it is decimated by eliminating predetermined lines so as to have the same number of lines as that of the receiver displaying apparatus, and be transferred to the image inverse-conversion processor. The image inverse-conversion processor includes a converter circuit common to the conversion to odd and even fields.

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